

REMARKS

This timely filed Reply to Office Action is responsive to the Office Action mailed April 25, 2005. Claims 12-19, drawn to a method of manufacturing a semiconductor device, were all allowed. However, the Examiner objected to the specification due to an inconsistency between the labeling of the figure numbers in the specification and on the drawing sheets. This objection relates to Figures 1A, 1B, 2A, 2B, 4A, 4B and 8A and 8B. For example, in the specification, Fig. "1(a)" is referred to on the drawing sheet as "Fig 1A".

In response, Applicants have amended the figure numbering in Figures 1A, 1B, 2A, 2B, 4A, 4B and 8A and 8B to become consistent to the figure numbering referenced in the specification (1(a), 1(b), etc.). Accordingly, the above objection is now overcome.

Applicants have made every effort to present claims which distinguish over the cited art, and it is believed that all claims are in condition for allowance. However, Applicants invite the Examiner to call the undersigned if it is believed that a telephonic interview (direct line (561) 671-3662) would expedite the prosecution of the application to an allowance.

No fees are believed due with the filing of the above Response. However, the Commissioner for Patents and Trademarks is hereby authorized to charge any deficiency in any fees due with the filing of this paper to Deposit Account No. 50-0951.

Respectfully submitted,

AKERMAN SENTERFITT

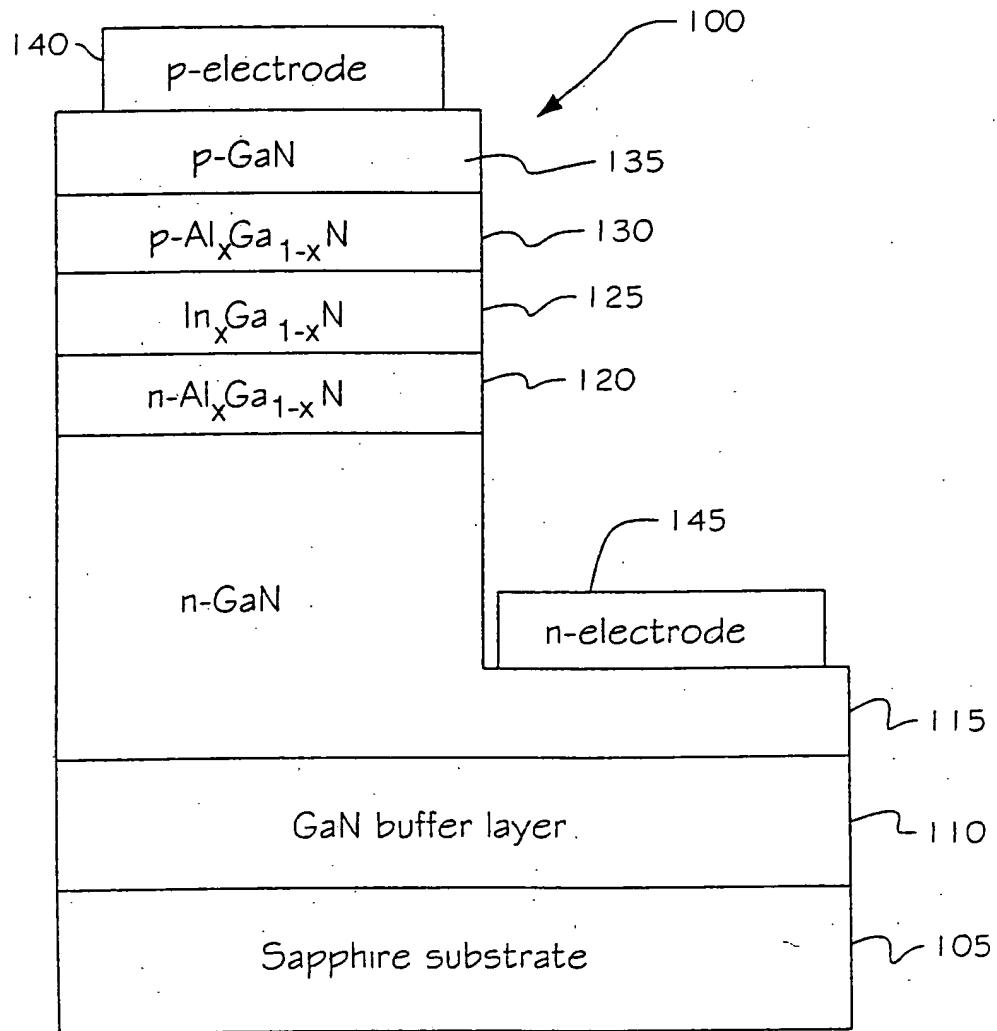
Date: July 17, 2005

Docket No. 5853-415

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FIG. 1(a)



PRIOR ART

FIG. 1(b)

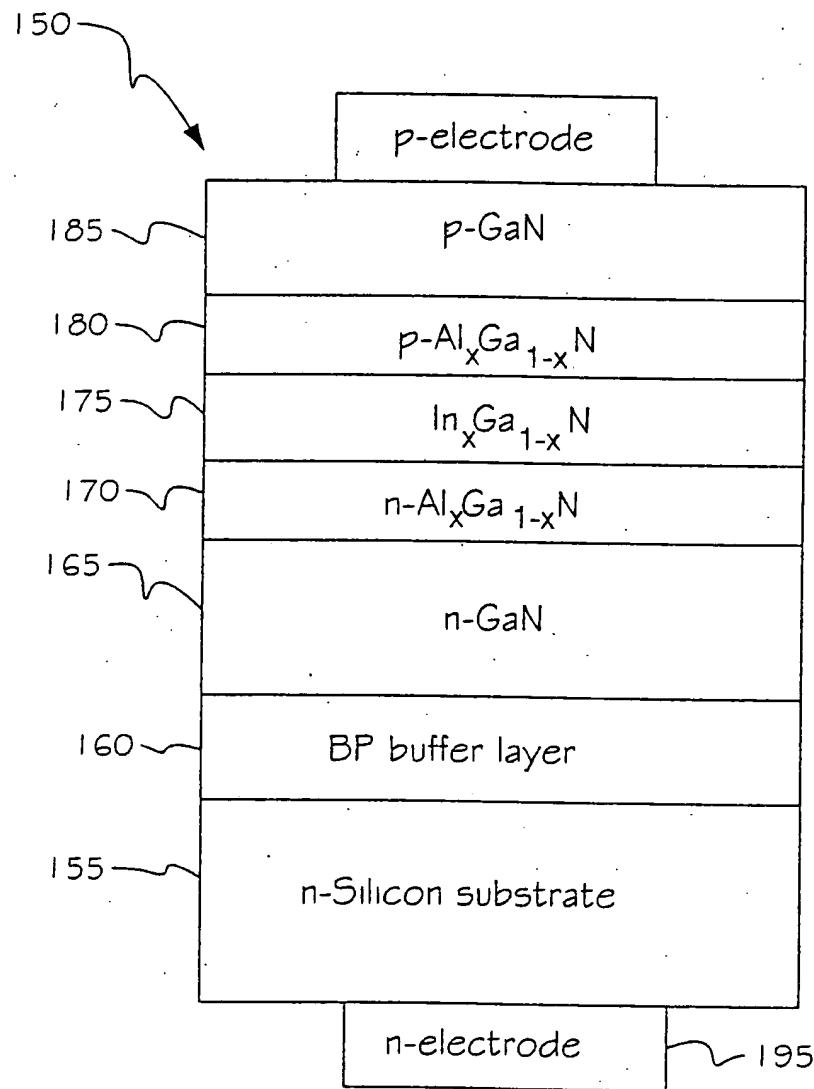


Fig. 2(a)

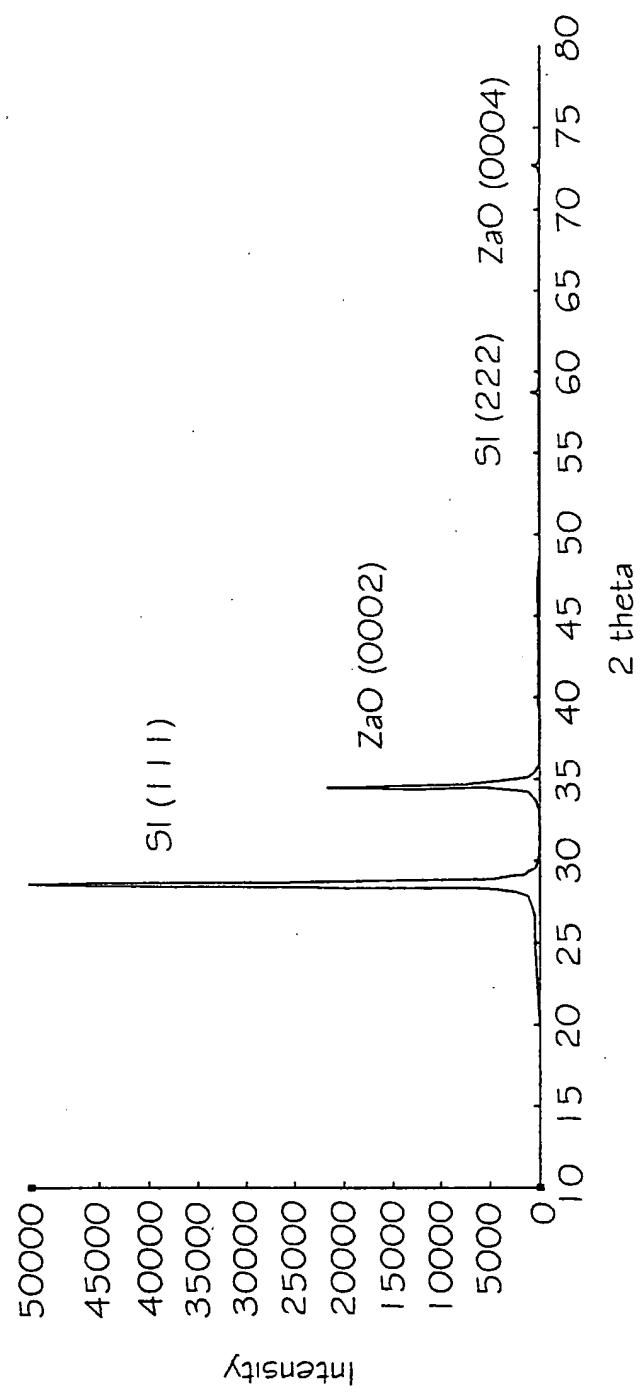


FIG. 2(b)

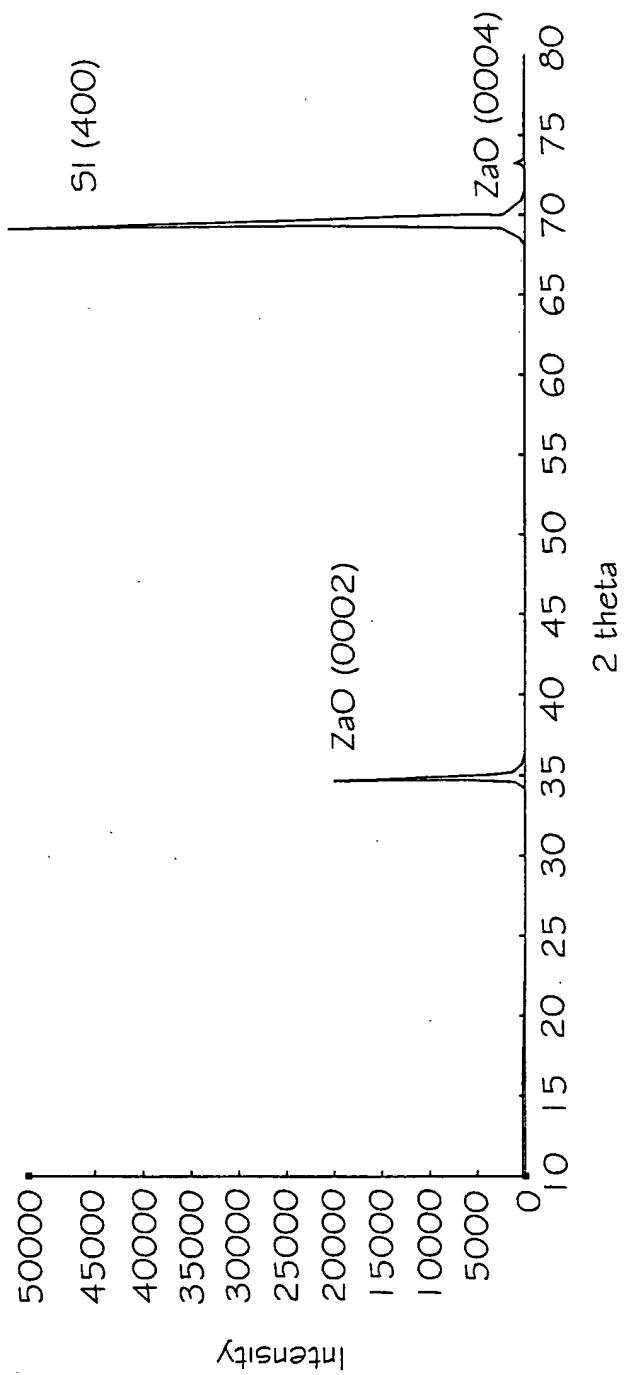


FIG. 4 (a)

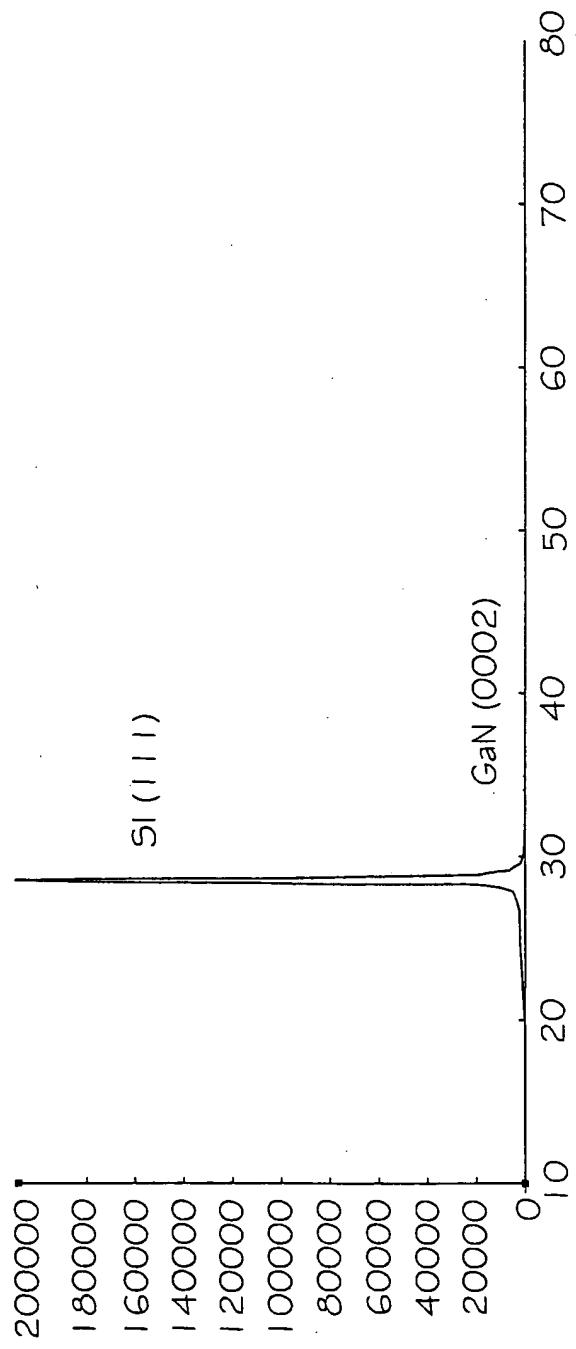
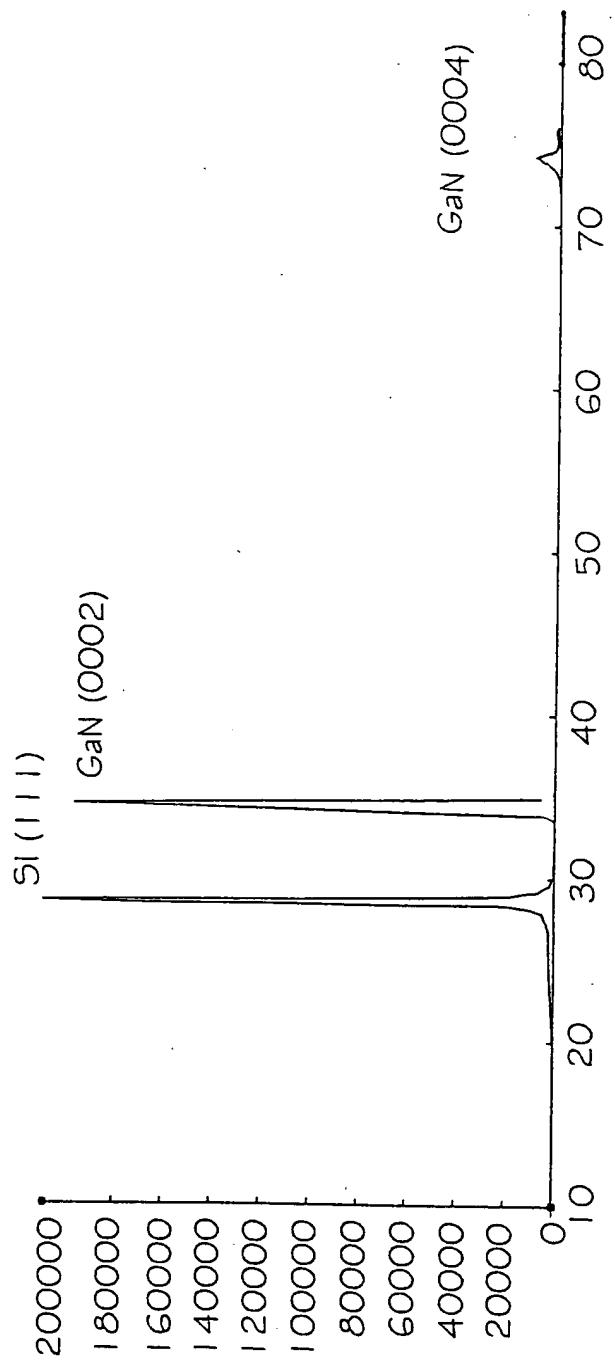
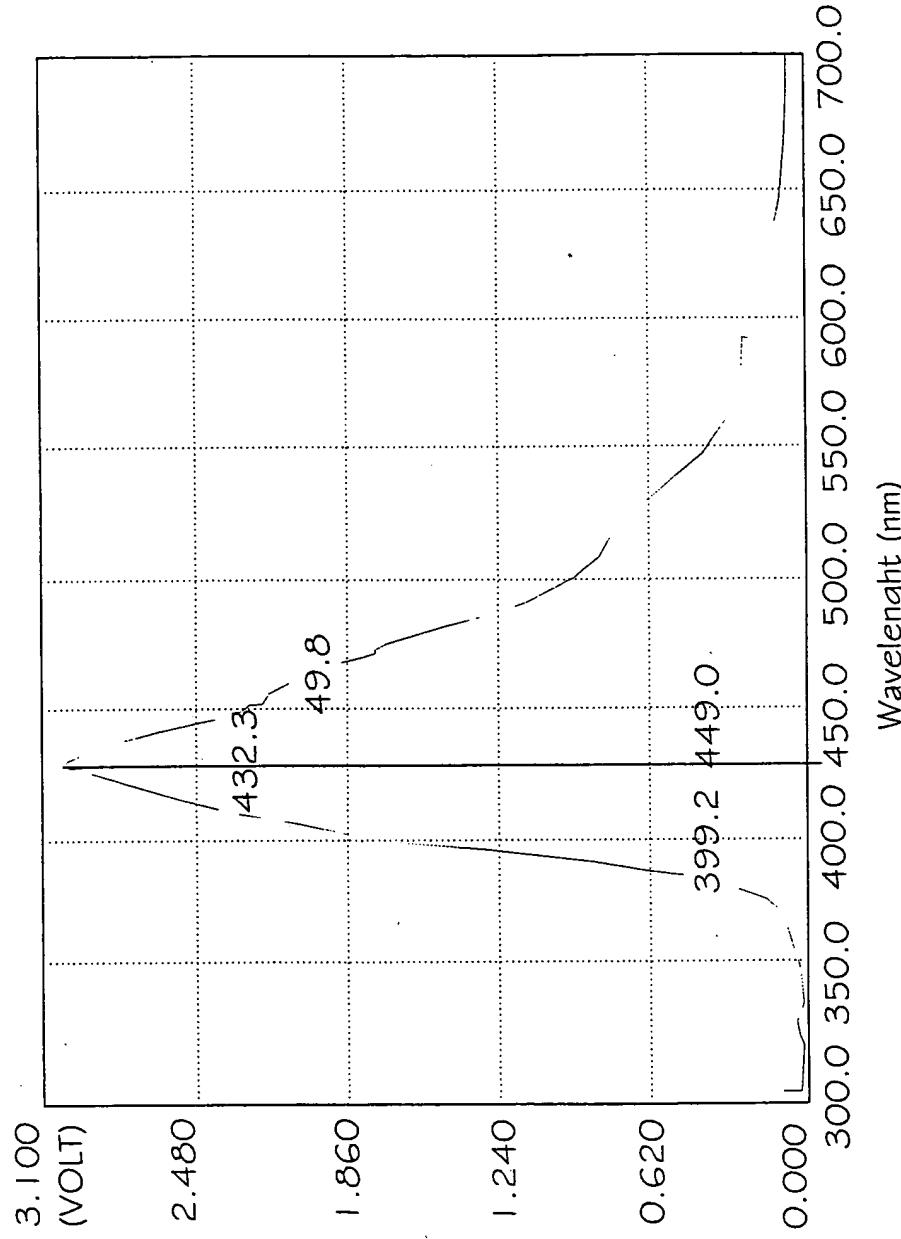


FIG. 4 (b)



Room Temperature PL of GaN/ZnO/Si



PL spectrum of GaN/ZnO/Si

Room Temperature PL of GaN/ZnO/Si

3.41 eV

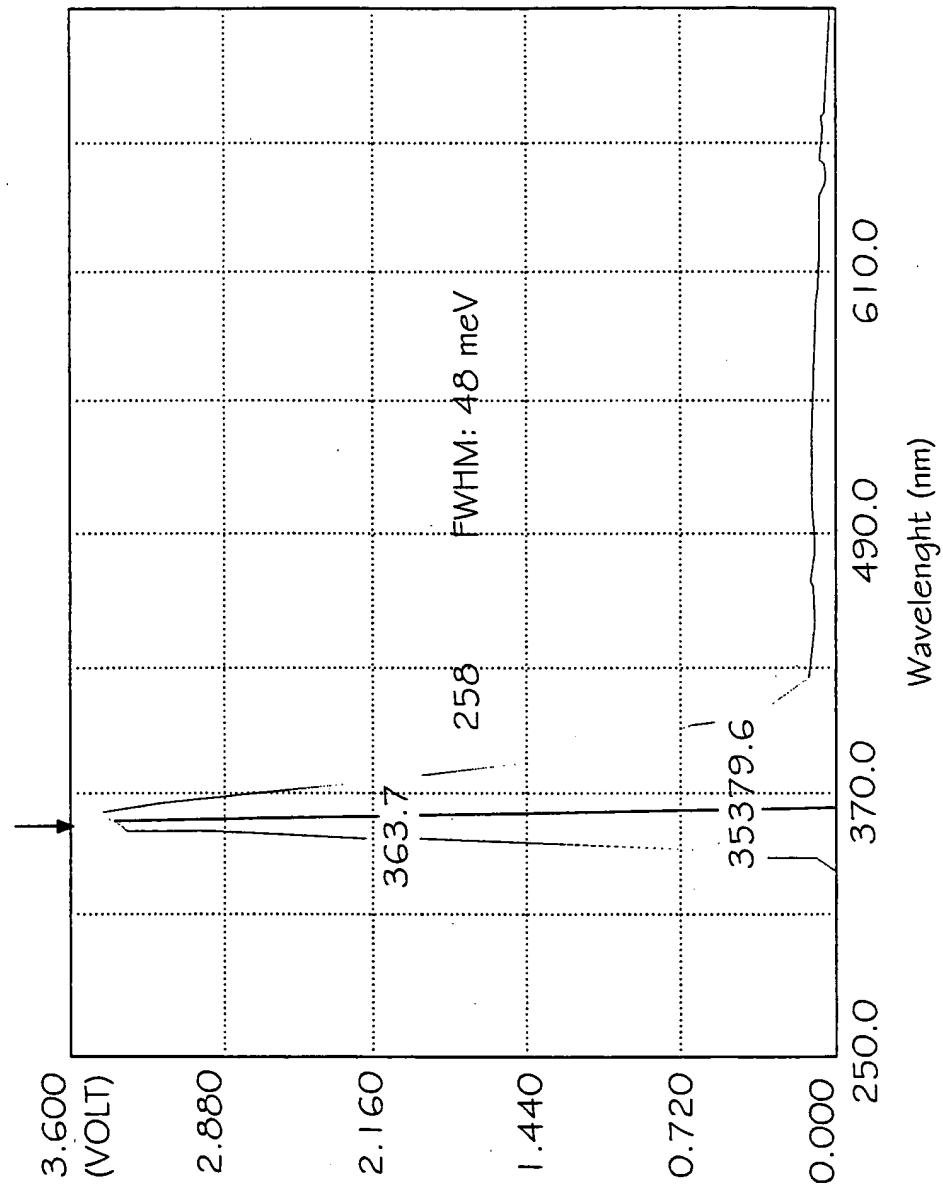


FIG. 8(b)

PL spectrum of GaN on bare Si